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**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q79663

Junichi YAMADA

Appln. No.: 10/773,290

Group Art Unit: 2818

Confirmation No.: 8732

Examiner: Ly D. PHAM

Filed: February 9, 2004

For: SEMICONDUCTOR MEMORY HAVING A DEFECTIVE MEMORY CELL  
RELIEVING CIRCUIT

**RESPONSE UNDER 37 C.F.R. § 1.111**

**MAIL STOP AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In response to the Office Action dated July 21, 2004, please consider the remarks as  
submitted herewith on the accompanying pages.

**TABLE OF CONTENTS**

REMARKS..... 2